## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Filed: Invento Han Nils Law Sher Leon | s Eberle Gura rence Spracklen ueling Chang-Shantz nard Rarick Method and Apparatus for Implementing Processor Instructions for |                | Examiner:<br>Group/Art Unit:<br>Atty. Dkt. No: | Johnson, Carlton<br>2136<br>6000-31500 |
|---------------------------------------|--|----------------|--|--|
|                                       | Instructions for Accelerating Public-Key Cryptography  | \$<br>\$<br>\$ |  |  |
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## **RESPONSE TO OFFICE ACTION OF JUNE 12, 2008**

## **Mail Stop Amendment**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## Dear Sir:

This paper is submitted in response to the Office Action of June 12, 2008 to further highlight why the application is in condition for allowance.

Please amend the case as listed below.